

ADVA225.001AUS



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Masahiro Ishida et al.) Group Art Unit 2631
Serial No. : 10/082,563)
Filed : February 23, 2002)
For : PROBABILITY ESTIMATING)
APPARATUS AND METHOD FOR)
PEAK-TO-PEAK CLOCK SKEWS)
Examiner : Unknown)

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

Enclosed is a form PTO-1449 listing two references relevant to the above-identified application. A copy of each reference is also enclosed. This information disclosure statement is filed before the first office action on the merits under 37 CFR 1.97(b)(3), and thus no fee is required in accordance with 37 C.F.R. § 1.97(b)(1). The prior art references enclosed herewith are:

(1) "Jitter Measurement of a PowerPC Microprocessor Using an Analytic Signal Method", Takahiro Yamaguchi et al., IEEE International Test Conference 2000, October 3-5, 2000,

(2) "Extraction of Peak-to-Peak and RMS Sinusoidal Jitter Using an Analytic Signal Method", Takahiro Yamaguchi et al., 18th IEEE VLSI Test Symposium, April 30-May 4, 2000.

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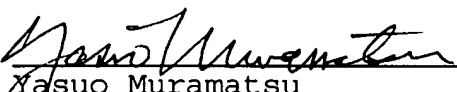
REMARKS

Applicant respectfully requests that the references disclosed in the statement be considered in the substantive examination of this case.

Respectfully submitted,

MURAMATSU & ASSOCIATES

Dated: 6/20/2002

By: 
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

of

Complete if Known

Application Number

10/082,563

Filing Date

2/23/2002

First Named Inventor

Masahiro Ishida

Group Art Unit

2631

Examiner Name

Attorney Docket Number

ADVA 225.00 / AUS

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

**Examiner
Initials:**

Cite
No.¹

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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"Jitter Measurement of a PowerPC Microprocessor Using an Analytic Signal Method" IEEE Test Conference 2000

"Extraction of Peak-to-Peak and RMS Sinusoidal Jitter Using an Analytic Signal Method" 18th VLSI Test Symposium

Examiner
Signature

Date _____

Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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